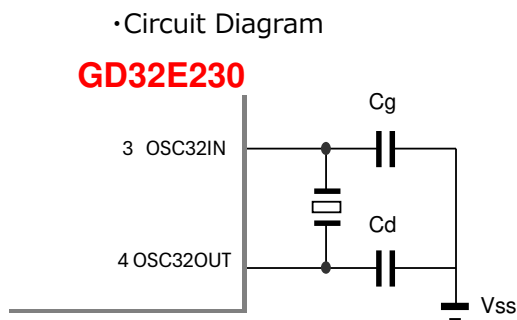


Giga Device semiconductor GD32E230 Series Matching Data Details

◆Evaluation items and evaluation data of oscillation circuit characteristics



Evaluation item

No	Item	Symbol	Recommended conditions
1	Frequency	$\Delta f/f$	reference data
2	Negative resistance	RL	The value shall be at least 5 times the product R1 specification value.
3	Oscillation margin	M	
4	Drive Level	D.L	Must be within product specifications.

To ensure safe use by our customers,

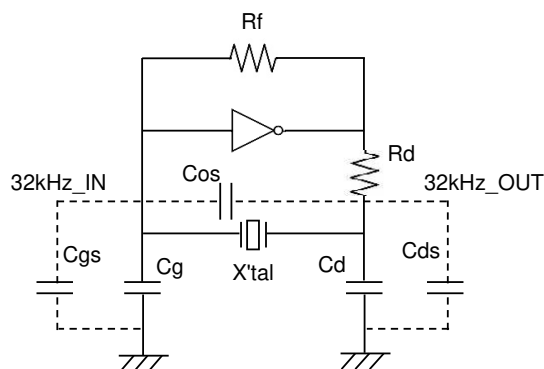
We recommend an oscillation margin of 5 times or more.

•Product and recommended circuit constants

IC Type	Mode	32kHz Crystal Unit			external element		Oscillation characteristics			
		Product name	R1Max. (k Ω)	CL (pF)	Cg (pF)	Cd (pF)	$\Delta f/f$ (ppm)	RL (k Ω)	M (times)	DL (μ W)
GD32E230	Idle	SSP-T7-F	65	12.5	18	18	-0.89	-624	9.6	0.07
	Sleep			12.5	18	18	-0.89	-624	9.6	0.07
	Deep-sleep			12.5	18	18	-0.89	-624	9.6	0.07
	Standby			12.5	18	18	-1.37	-624	9.6	0.07
	Idle	SC-32S	70	12.5	18	18	3.39	-640	9.1	0.07
	Sleep			12.5	18	18	3.39	-640	9.1	0.07
	Deep-sleep			12.5	18	18	3.39	-640	9.1	0.07
	Standby			12.5	18	18	2.20	-640	9.1	0.07
	Idle	SC-20S	70	12.5	20	20	-0.31	-543	7.8	0.07
	Sleep			12.5	20	20	-0.31	-543	7.8	0.07
	Deep-sleep			12.5	20	20	-0.31	-543	7.8	0.07
	Standby			12.5	20	20	-2.47	-543	7.8	0.07
	Idle	SC-16S	90	12.5	20	22	2.35	-497	5.5	0.06
	Sleep			12.5	20	22	2.35	-497	5.5	0.06
	Deep-sleep			12.5	20	22	2.35	-497	5.5	0.06
	Standby			12.5	20	22	-0.46	-497	5.5	0.06

Please contact us for other products not listed above.

◆About circuit load capacitance



The oscillation circuit has stray capacitance.
The CL value is set considering stray capacitance.
 $CL = C_g \times C_d / (C_g + C_d) + C_s$ (pF)

C_s ≡ Circuit stray capacitance

What is floating capacity?

C_{os} : 32kHz_IN-32kHz_OUT Stray capacitance

C_{gs} : 32kHz_IN-Vss Stray capacitance

C_{ds} : 32kHz_OUT-Vss Stray capacitance

◆Circuit board design considerations

Place the crystal unit, capacitors, and resistors as close to the Chip as possible to shorten the wiring.
To prevent interference with other signal lines, do not place other signal lines in the area where the crystal unit is mounted (underside).

The oscillation circuit design is described on our website.

In addition, please use our circuit matching service. For details, please contact our sales representatives or visit our website.

◆Caution

The above evaluation results are reference values evaluated on specific samples and "IC manufacturer's evaluation board",

They are subject to change depending on the customer's board design.

Please note that the capacitance values and characteristics of external elements may vary depending on differences in stray capacitance and other factors in actual circuit boards.